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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/829,668	04/22/2004	Douglas C. Burger	119832-163869	6831
60/172,750	10/07/2010	SCHWABE, WILLIAMSON & WYATT, P.C.	EXAMINER	
1420 FIFTH, SUITE 3400			FENNEMA, ROBERT E	
SEATTLE, WA 98101-4010			ART UNIT	PAPER NUMBER
			2183	
			MAIL DATE	DELIVERY MODE
			10/07/2010	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

**Advisory Action
Before the Filing of an Appeal Brief**

Application No. 10/829,668	Applicant(s) BURGER ET AL.
Examiner Robert Fennema	Art Unit 2183

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 22 September 2010 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☐ The period for reply expires _____ months from the mailing date of the final rejection.
b) ☒ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.
Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. ☐ The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
(a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);
(b) ☐ They raise the issue of new matter (see NOTE below);
(c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
(d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
5. ☒ Applicant's reply has overcome the following rejection(s): The rejections under 35 U.S.C. 101.
6. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
7. ☒ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
The status of the claim(s) is (or will be) as follows:
Claim(s) allowed: _____.
Claim(s) objected to: _____.
Claim(s) rejected: 37-44, 46-50, 52-58 and 60.
Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.
12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s) _____.
13. ☒ Other: See Continuation Sheet

/Robert Fennema/
Examiner
Art Unit: 2183

Continuation of 11. does NOT place the application in condition for allowance because: Applicant has argued that in Requa, there is no concept of blocks of instructions being "assigned" to the processors. Applicant has argued that the instruction issue section of Requa determines which processor will execute a given instruction based on the instruction type. Applicant has also argued that Requa does not teach a distributed store on each processor, to which Examiner agrees, which is (in part) as to why Patterson has been combined with Requa, to teach the distributed instruction stores. However, Examiner would argue, that even in the non-distributed case, if the instruction issue section determines which processor is to execute an instruction, based on instruction type, that it is still an assignment. To the Examiner, "assigning" an instruction to a processor means determining that the processor is to execute the instruction, which clearly occurs, the instruction issue logic must determine that a scalar instruction goes to the scalar processor and not the memory processor. Even if this assignment is pre-determined because of the instruction type, it is still an assignment. There is nothing in the claims which indicate that the assignment has any other limitations, so as the Examiner interprets the claim, Examiner does not see how an assignment step does not occur, and it appears to almost be inherent that if a processor loads an instruction to be executed, that the instruction was assigned to it. It is possible that in the Applicant's inventive concept, the assignment step is significantly more complex, and differentiates from the Examiners interpretation, but as currently claimed, Examiner believes that Requa clearly teaches the assignment step, and is not persuaded by the Applicant's arguments to the contrary. If the assignment step is in fact more complex than the Examiner is making it out to be, Examiner suggests more explicitly claiming said step to differentiate from the art, but appears to read the step in a much broader sense than the Applicant currently is.

Continuation of 13. Other: Examiner notes that the amendment to the claims have overcome the claim objections and the rejections under 101..